

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			
<b>SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT</b>		Docket Number: 2885/86	Confirmation Number: 9148
Application Number 10/501,845	Filing Date August 26, 2005	Examiner Keith E. Vicary	Art Unit 2183
Invention Title <b>RECONFIGURABLE GENERAL PURPOSE PROCESSOR HAVING TIME RESTRICTED CONFIGURATIONS</b>		Inventors <b>Martin VORBACH et al.</b>	

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

I hereby certify that this correspondence is being electronically transmitted to the United States Patent and Trademark Office via the Office electronic filing system on  
Date: November 3, 2011  
Signature: Bumjee Kim/  
Bumjee Kim

Sir:

Pursuant to 37 CFR § 1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached List of References. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom. The filing of this Information Disclosure Statement and the List of References, shall not be construed as an admission that the information cited is prior art, or is considered to be material to patentability as defined in 37 C.F.R. § 1.56(b). The paragraphs marked below are applicable. It is believed that no fees other than those indicated below are due, but authorization is hereby given to charge any additional fees due, or to credit any overpayment, to Kenyon & Kenyon LLP, deposit account 11-0600.

This Information Disclosure Statement brings to the attention of the U.S. Patent and Trademark Office information that arose in the following patent litigation: *PACT XPP Technologies, AG v. XILINX, Inc., et al., (E.D. Texas Dec. 28, 2007) (No. 2:07cv563)*.

☒ 1. This Information Disclosure Statement is being filed (a) within three months of the filing date of a national application other than a continued prosecution application under 37 C.F.R. § 1.53(d), (b) within three months of the date of entry of the national stage as set forth in 37 C.F.R. § 1.491 in an international application, (c) before the mailing date of a first Office Action on the merits in the present application, OR (d) before the mailing of a first office action after filing of a request for continued examination. No certification or fee is required.

☐ 2. This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a final action, Notice of Allowance, or any action that otherwise closes prosecution.

☐ a. I hereby certify that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 CFR § 1.97(e)(1).

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /K.V./

☐ b. I hereby certify that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(2).

☐ c. The required fee of **\$180.00** under 37 CFR §1.17(p) is being paid by **credit card** to ensure consideration of the disclosed information. Any additional fees may be charged to Deposit Account No. 11-0600 of Kenyon & Kenyon LLP

☒ 3. Since this application was filed after June 30, 2003, copies of U.S. references are not included.

Respectfully submitted,

Date: November 3, 2011

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**INFORMATION DISCLOSURE  
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**U.S. PATENT DOCUMENTS**

EXAMINER'S INITIALS	Patent Number	Publication Date	Patentee	CLASS	SUBCLASS	FILING DATE
	5,055,997	Oct 8, 1991	Sluijter et al.			
	5,682,491	Oct 28, 1997	Pechanek et al.			
	2009/0193384	Jul 30, 2009	Sima et al.			

**FOREIGN PATENT DOCUMENTS**

EXAMINER'S INITIALS	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

**OTHER DOCUMENTS**

EXAMINER'S INITIALS	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
	(1) Microsoft Press Computer Dictionary, Third Edition, Redmond, WA, 1997, 3 pages.
	(2) Microsoft Press Computer Dictionary, Second Edition, Redmond, WA, 1994, 3 pages.
	(3) A Dictionary of Computing, Fourth Edition, Oxford University Press, 1997, 4 pages.
	(4) Communications Standard Dictionary, Third Edition, Martin Weik (Ed.), Chapman & Hall, 1996, 3 pages.
	(5) Dictionary of Communications Technology, Terms Definitions and Abbreviations, Second Edition, Gilbert Held (Ed.), John Wiley & Sons, England, 1995, 5 pages.
	(6) The Random House College Dictionary, Revised Edition, Random House, Inc., 1984, 14 pages.
	(7) The Random House College Dictionary, Revised Edition, Random House, Inc., 1984, 7 pages.
	(8) Random House Webster's College Dictionary with CD-ROM, Random House, 2001, 7 pages.
	(9) Random House Webster's College Dictionary with CD-ROM, Random House, 2001, 4 pages.
	(10) Random House Personal Computer Dictionary, Second Edition, Philip E. Margolis (Ed.), Random House, New York, 1996, 5 pages.
	(11) The IEEE Standard Dictionary of Electrical and Electronics Terms, Sixth Edition, 1996, 36 pages.
	(12) The IEEE Standard Dictionary of Electrical and Electronics Terms, Sixth Edition, 1996, 8 pages.
	(13) McGraw-Hill Electronics Dictionary, Sixth Edition, Neil Slater et al. (Ed.), McGraw-Hill, 1997, 3 pages.
	(14) Modern Dictionary of Electronics, Sixth Edition, Rudolf Graf (Ed.), Newnes (Butterworth-Heinemann), 1997, 5 pages.
	(15) The American Heritage Dictionary, Fourth Edition, Dell (Houghton-Mifflin), 2001, 5 pages.
	(16) The American Heritage Dictionary, Second College Edition, Houghton Mifflin, 1982, 23 pages.
	(17) The American Heritage Dictionary, Second College Edition, Houghton Mifflin, 1982, 8 pages.
	(18) The American Heritage Dictionary, Third Edition, Dell Publishing (Bantam Doubleday Dell Publishing Group, Inc.), 1994, 4 pages.
	(19) The American Heritage Dictionary, Fourth Edition, Dell/Houghton Mifflin 2001, 5 pages.
	(20) Webster's New Collegiate Dictionary, Merriam Co., 1981, 5 pages.
	(21) Webster's New Collegiate Dictionary, Merriam Co., 1981, 4 pages.
	(22) The Oxford American Dictionary and Language Guide, Oxford University Press, 1999, 5 pages.
	(23) The Oxford Duden German Dictionary, Edited by the Dudenredaktion and the German Section of the Oxford University Press, W. Scholze-Stubenrecht et al. (Eds), Clarendon Press, Oxford, 1990, 7 pages.
	(24) Oxford Dictionary of Computing, Oxford University Press, 2008, 4 pages.

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EXAMINER'S INITIALS	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
	(25) Modern Dictionary of Electronics, Sixth Edition Revised and Updated, Rudolf F. Graf (Ed.), Butterworth-Heinemann, 1997, 7 pages.
	(26) Modern Dictionary of Electronics, Sixth Edition Revised and Updated, Rudolf F. Graf (Ed.), Butterworth-Heinemann, 1997, 5 pages.
	(27) Garner's Modern American Usage, Bryan A. Garner (Ed.), Oxford University Press, 2003, 3 pages.
	(28) The New Fowler's Modern English Usage, R.W. Burchfield (Ed.), Oxford University Press, 2000, 3 pages.
	(29) Wikipedia, the free encyclopedia, "Granularity," at <a href="http://en.wikipedia.org/wiki/Granularity">http://en.wikipedia.org/wiki/Granularity</a> , 6/18/2010, 4 pages.
	(30) Wordsmyth, The Premier Educational Dictionary – Thesaurus, at <a href="http://www.wordsmyth.net">http://www.wordsmyth.net</a> , "communication", 6/18/2010, 1 page.
	(31) Yahoo! Education, "affect," at <a href="http://education.yahoo.com/reference/dictionary/entry/affect">http://education.yahoo.com/reference/dictionary/entry/affect</a> , 6/18/2010, 2 pages.
	(32) mPulse Living Language, "high-level," at <a href="http://www.macmillandictionary.com/dictionary/american/high-level">http://www.macmillandictionary.com/dictionary/american/high-level</a> , 6/18/2010, 1 page.
	(33) MSN Encarta, "regroup," at <a href="http://encarta.msn.com/encnet/features/dictionary/DictionaryResults.aspx?textype=3&amp;search=regroup">http://encarta.msn.com/encnet/features/dictionary/DictionaryResults.aspx?textype=3&amp;search=regroup</a> , 6/17/2010, 2 pages.
	(34) MSN Encarta, "synchronize," at <a href="http://encarta.msn.com/encnet/features/dictionary/DictionaryResults.aspx?textype=3&amp;search=synchronize">http://encarta.msn.com/encnet/features/dictionary/DictionaryResults.aspx?textype=3&amp;search=synchronize</a> , 6/17/2010, 2 pages.
	(35) MSN Encarta, "pattern," at <a href="http://encarta.msn.com/encnet/features/dictionary/DictionaryResults.aspx?textype=3&amp;search=pattern">http://encarta.msn.com/encnet/features/dictionary/DictionaryResults.aspx?textype=3&amp;search=pattern</a> , 6/17/2010, 2 pages.
	(36) MSN Encarta, "dimension," at <a href="http://encarta.msn.com/encnet/features/dictionary/DictionaryResults.aspx?textype=3&amp;search=dimension">http://encarta.msn.com/encnet/features/dictionary/DictionaryResults.aspx?textype=3&amp;search=dimension</a> , 6/17/2010, 2 pages.
	(37) MSN Encarta, "communication," at <a href="http://encarta.msn.com/encnet/features/dictionary/DictionaryResults.aspx?textype=3&amp;search=communication">http://encarta.msn.com/encnet/features/dictionary/DictionaryResults.aspx?textype=3&amp;search=communication</a> , 6/17/2010, 2 pages.
	(38) MSN Encarta, "arrangement," at <a href="http://encarta.msn.com/encnet/features/dictionary/DictionaryResults.aspx?textype=3&amp;search=arrangement">http://encarta.msn.com/encnet/features/dictionary/DictionaryResults.aspx?textype=3&amp;search=arrangement</a> , 6/17/2010, 2 pages.
	(39) MSN Encarta, "vector," at <a href="http://encarta.msn.com/encnet/features/dictionary/DictionaryResults.aspx?textype=3&amp;search=vector">http://encarta.msn.com/encnet/features/dictionary/DictionaryResults.aspx?textype=3&amp;search=vector</a> , 7/30/2010, 2 pages.
	(40) Dictionary.com, "address," at <a href="http://dictionary.reference.com/browse/address">http://dictionary.reference.com/browse/address</a> , 6/18/2010, 4 pages.
	(41) P.R. 4-3 Joint Claim Constructions Statement, <i>PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al.</i> , E.D. Texas, 2:07-cv-00563-CE, July 19, 2010, pp. 1-50.
	(42) Order Granting Joint Motion for Leave to File An Amended Joint Claim Construction and Prehearing Statement and Joint Motion to File an Amended Joint Claim Construction and Prehearing Statement Pursuant to Local Patent Rule 4-3, and Exhibit A: P.R. 4-3 Amended Joint Claim Constructions Statement, <i>PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al.</i> , E.D. Texas, 2:07-cv-00563-CE, August 2, 2010, 72 pages.
	(43-1) P.R. 4-3 Amended Joint Claim Constructions Statement, <i>PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al.</i> , E.D. Texas, 2:07-cv-00563-CE, August 3, 2010, pages 1-65.
	(43-2) Exhibit A - P.R. 4-3 Amended Joint Claim Constructions Statement, <i>PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al.</i> , E.D. Texas, 2:07-cv-00563-CE, August 2, 2010, pages 1-66.
	(44) PACT's Opening Claim Construction Brief, <i>PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc.</i> , E.D. Texas, 2:07-cv-00563-CE, November 1, 2010, pp. 1-55.
	(45) Declaration of Harry L. (Nick) Tredennick in Support of PACT's Claim Constructions, <i>PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al.</i> , E.D. Texas, 2:07-cv-00563-CE, November 1, 2010, pp. 1-87.
	(46) Transcript of Harry (Nick) L. Tredennick III, Ph.D., October 11, 2010, Vol. 1, Exhibit 16 of PACT's Opening Claim Construction Brief, <i>PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al.</i> , E.D. Texas, 2:07-cv-00563-CE, November 1, 2010, pp. 1-3.
	(47) Agreed and Disputed Terms, Exhibit 17 of PACT's Opening Claim Construction Brief, <i>PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al.</i> , E.D. Texas, 2:07-cv-00563-CE, November 1, 2010, pp. 1-16.
	(48) Oral Videotaped Deposition – Joseph McAlexander dated October 12, 2010, Vol. 1, Exhibit 18 of PACT's Opening Claim Construction Brief, <i>PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al.</i> , E.D. Texas, 2:07-cv-00563-CE, November 1, 2010, pp. 1-17.
	(49) Expert Report of Joe McAlexander Re Claim Construction dated September 27, 2010, Exhibit 19 of PACT's Opening Claim Construction Brief, <i>PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al.</i> , E.D. Texas, 2:07-cv-00563-CE, November 1, 2010, pp. 1-112.

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EXAMINER'S  
INITIALS

AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.

- (50) Documents from File History of U.S. Patent Application Serial No. 09/290,342 (filed April 12, 1999), Exhibit 20 of PACT's Opening Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, November 1, 2010, pp. 1-37.
- (51) Amendment from File History of U.S. Patent Application Serial No. 10/156,397 (filed May 28, 2002), Exhibit 25 of PACT's Opening Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, November 1, 2010, pp. 1-12.
- (52) Documents from File History U.S. Patent Application Serial No. 09/329,132 (filed June 9, 1999), Exhibit 27 of PACT's Opening Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, November 1, 2010, pp. 1-36.
- (53) Amendment from File History of U.S. Patent Application Serial No. 10/791,501 (filed March 1, 2004), Exhibit 39 of PACT's Opening Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, November 1, 2010, pp. 1-9.
- (54) Amendment from File History of U.S. Patent Application Serial No. 10/265,846 (filed October 7, 2002), Exhibit 40 of PACT's Opening Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, November 1, 2010, pp. 1-12.
- (55) Defendants Xilinx, Inc. and Avnet, Inc.'s Responsive Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, December 6, 2010, pp. 1-55.
- (56) Declaration of Aaron Taggart in Support of Defendants Xilinx, Inc. and Avnet, Inc.'s Responsive Claim Construction Brief, Defendants Xilinx, Inc. and Avnet, Inc.'s Responsive Claim Construction Brief (Exhibit A), *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, December 6, 2010, pp. 1-5.
- (57) Oral Videotaped Deposition Joseph McAlexander (October 12, 2010), Exhibit 1 of Defendants Xilinx, Inc. and Avnet, Inc.'s Responsive Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, December 6, 2010, pp. 1-9.
- (58) Expert Report of Joe McAlexander re Claim Construction, Exhibit 2 of Defendants Xilinx, Inc. and Avnet, Inc.'s Responsive Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, December 6, 2010, pp. 1-137.
- (59) Various Documents from File History of U.S. Patent Application Serial No. 09/290,342 (filed 4/12/99), Exhibit 6 of Defendants Xilinx, Inc. and Avnet, Inc.'s Responsive Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, December 6, 2010, pp. 1-81.
- (60) Transcript of Harry (Nick) L. Tredennick III, Ph.D., October 11, 2010, Vol. 1, Exhibit 7 of Defendants Xilinx, Inc. and Avnet, Inc.'s Responsive Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, December 6, 2010, pp. 1-28.
- (61) Amendment, Response from File History of U.S. Patent Application Serial No. 10/156,397 (filed May 28, 2002), Exhibit 15 of Defendants Xilinx, Inc. and Avnet, Inc.'s Responsive Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, December 6, 2010, pp. 1-137.
- (62) Application from File History of U.S. Patent Application Serial No. 08/544,435 (filed November 17, 1995), Exhibit 20 of Defendants Xilinx, Inc. and Avnet, Inc.'s Responsive Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, December 6, 2010, pp. 1-102.
- (63) Documents from File History of U.S. Patent Application Serial No. 09/329,132 (filed June 9, 1999), Exhibit 24 of Defendants Xilinx, Inc. and Avnet, Inc.'s Responsive Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, December 6, 2010, pp. 1-13.
- (64) Documents from File History of U.S. Patent Application Serial No. 10/791,501 (filed March 1, 2004), Exhibit 25 of Defendants Xilinx, Inc. and Avnet, Inc.'s Responsive Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, December 6, 2010, pp. 1-14.
- (65) Amendment from File History of U.S. Patent Application Serial No. 11/246,617 (filed October 7, 2005), Exhibit 26 of Defendants Xilinx, Inc. and Avnet, Inc.'s Responsive Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, December 6, 2010, pp. 1-9.
- (66-1) Documents from File History of U.S. Patent Application Serial No. 08/947,254 (filed October 8, 1997), Exhibit 27 of Defendants Xilinx, Inc. and Avnet, Inc.'s Responsive Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, December 6, 2010, pp. 1-38.
- (66-2) Documents from File History of U.S. Patent Application Serial No. 08/947,254 (filed October 8, 1997), specifically, German priority application specification [English translation provided], Exhibit 33 of Defendants Xilinx, Inc. and Avnet, Inc.'s Responsive Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, December 6, 2010, pp. 1-38.

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- (67) Documents from File History of U.S. Patent Application Serial No. 09/335,974 (filed June 18, 1999), Exhibit 28 of Defendants Xilinx, Inc. and Avnet, Inc.'s Responsive Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, December 6, 2010, pp. 1-32.
- (68) Documents from File History of U.S. Patent Reexamination Control No. 90/010,450 (filed March 27, 2009), Exhibit 30 of Defendants Xilinx, Inc. and Avnet, Inc.'s Responsive Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, December 6, 2010, pp. 1-71.
- (69) Documents from File History of U.S. Patent Application Serial No. 10/265,846 (filed October 7, 2002), Exhibit 32 of Defendants Xilinx, Inc. and Avnet, Inc.'s Responsive Claim Construction Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, December 6, 2010, pp. 1-23.
- (70) PACT's Claim Construction Reply Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, January 7, 2011, pp. 1-20.
- (71) Defendants Xilinx, Inc. and Avnet, Inc.'s Claim Construction Surreply Brief, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, January 18, 2011, 142 pages.
- (72) Markman Hearing Minutes and Attorney Sign-In Sheet, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, February 22, 2011, 3 pages; and court transcript, 245 pages.
- (73) Memorandum Opinion and Order, *PACT XPP Technologies, AG v. Xilinx, Inc. and Avnet, Inc. et al., E.D. Texas, 2:07-cv-00563-CE*, June 17, 2011, pp. 1-71.
- (74) Atmel Corporation, Atmel 5-K- 50K Gates Coprocessor FPGA and FreeRAM, ([www.atmel.com](http://www.atmel.com)), April 2002, pp. 1-68.
- (75) Glaskowsky, Peter N., "PACT Debuts Extreme Processor; Reconfigurable ALU Array Is Very Powerful – and Very Complex" Microprocessor, The Insider's Guide to Microprocessor Hardware, MicroDesign Resources – Microprocessor Report, October 9, 2000 ([www.MPRonline.com](http://www.MPRonline.com)), 6 pages.
- (76) Glaskowsky, Peter N., "Analysis' Choice Nominees Named; Our Picks for 2002's Most Important Products and Technologies" Microprocessor, The Insider's Guide to Microprocessor Hardware, MicroDesign Resources – Microprocessor Report, December 9, 2002 ([www.MPRonline.com](http://www.MPRonline.com)), 4 pages.
- (77) Lattice Semiconductor Corporation, ispLSI 2000E, 2000VE and 2000 VL Family Architectural Description, October 2001, pp. 1-88.
- (78) Olukotun, K. et al., "Rationale, Design and Performance of the Hydra Multiprocessor," Computer Systems Laboratory, Stanford University, CA, November 1994, pp. 1-19.
- (79) PACT Corporate Backgrounder, PACT company release, October 2008, 4 pages.
- (80) Page, Ian., "Reconfigurable processor architectures," Oxford University Computing Laboratory, Oxford UK, Elsevier Science B.V., *Microprocessors and Microsystems* 20 (1996) pp. 185-196.
- (81) Singh, Hartej et al., "Morpho-Sys: A Reconfigurable Architecture for Multimedia Applications," Univ. of California, Irvine, CA and Federal University of Rio de Janeiro, Brazil, at <http://www.eng.uci.edu/morphosys/docs/sbcci98.html>, June 18, 2010, 10 pages.
- (82) Theodoridis, G. et al., "Chapter 2 – A Survey of Coarse-Grain Reconfigurable Architectures and Cad Tools, Basic Definitions, Critical Design Issues and Existing Coarse-grain Reconfigurable Systems," from S. Vassiliadis, and D. Soudris (eds.) *Fine- and Coarse-Grained Reconfigurable Computing*, Springer 2007, pp. 89-149.
- (83) Weinhardt, Markus et al., "Using Function Folding to Improve Silicon Efficiency of Reconfigurable Arithmetic Arrays," PACT XPP Technologies AG, Munich, Germany, IEEE 2004, pp. 239-245.
- (84) Xilinx, XC6200 Field Programmable Gate Arrays, Advance Product Specification, June 1, 1996 (Version 1.0), pp. 4-255 through 4-286.
- (85-1) (85-2) (85-3) Xilinx, Virtex-II Platform FPGA User Guide, UG002 (V2.1) 28 March, 2007, pp. 1-502 [Parts 1-3]
- (86) Xilinx, XC4000E and SC4000X Serial Field Programmable Gate Arrays, Product Specification (Version 1.6), May 14, 1999, pp. 1-107.

EXAMINER

/Keith Vicary/

DATE CONSIDERED

12/27/2011

EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.